

JAN 25 2006

N1085-90179
TSMC2002-0115

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FACSIMILE TRANSMITTAL SHEET

In re application of: **Yeu-Der Chih**

Examiner: **Huan Hoang**

Serial No.: **10/785,522**

Group Art Unit: **2827**

Filed: **02/24/2004**

Confirmation No.: **8308**

**For: AN ARRAY STRUCTURE OF TWO-TRANSISTOR CELLS WITH MERGED
FLOATING GATES FOR BYTE ERASE AND RE-WRITE IF DISTURBED
ALGORITHM**

TO: Examiner Huan Hoan
Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

FACSIMILE NUMBER: 571-273-8300

CONFIRMATION

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DATE: January 25, 2006

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MESSAGE: Attached is the Response To Office Action.

NOTE: Original will not follow

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JAN 25 2006

Appl. No. 10/785,522
Amdt. dated 01/25/2006
Response to Office Action of 11/29/2005

Attorney Docket No.: N1085-90179
TSMC2002-0115

IN THE UNITED STATES PATENT AND TRADEMARK OFFICEIn re application of: **Yeu-Der Chih**Examiner: **Huan Hoang**Serial No.: **10/785,522**Group Art Unit: **2827**Filed: **02/24/2004**Confirmation No.: **8308**

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Date: Jan. 25, 2006

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RESPONSE TO OFFICE ACTION DATED NOVEMBER 29, 2005**EXPEDITED PROCEDURE UNDER 37 C.F.R. 1.116**

Sir:

Responsive to the final Office Action dated November 29, 2005, please re-examine and reconsider the above-identified application according to the amendments and remarks provided.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 12 of this paper.